

**AMENDMENTS TO THE SPECIFICATION****In the Specification:**

Please replace paragraph [0058] with amended paragraph [0058].

[0058]        Although the base stream LLRs may be stored with additional bits for the second data detection scheme, the total memory requirement may still be significantly less than that of the first data detection scheme, which stores both the received symbols and the base stream LLRs. This is especially true since the received symbols are also likely to require a larger bit-width in the presence of the enhancement stream.